



FORM PTO-1449		ATTY. DOCKET NO. I-2-0359US	SERIAL NO. 10/648,663
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT Gazda et al.	
		FILING DATE August 26, 2003	GROUP 2193

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,493,692	02/1996	Theimer et al.			
		5,555,416	09/1996	Owens et al.			
		5,946,487	08/1999	Dangelo			
		6,029,000	02/2000	Woolsey et al.			
		6,226,788	05/2001	Schoening et al.			
	*	6,256,637	07/2001	Venkatesh et. al.			
		6,256,775	07/2001	Flynn			
		6,263,213	07/2001	Kovacs			
	*	6,272,519	08/2001	Shearer et al.			
		6,407,751	06/2002	Minami et al.			
		6,493,740	12/2002	Lomax			
		6,505,228	01/2003	Schoening et al.			
		6,606,742	08/2003	Orton et al.			
		6,631,516	10/2003	Baumgart et al.			
		6,711,739	03/2004	Kutcher			
		6,721,945	04/2004	Sinha			
		6,725,451	04/2004	Schuetz et al.			
		6,754,885	06/2004	Dardinski et al.			
		6,804,682	10/2004	Kemper et al.			
		6,807,548	10/2004	Kemper			
	*	6,813,764	11/2004	Stoodley			
		6,842,433	01/2005	West et al.			
		6,851,110	02/2005	Hundt et al.			
		6,854,108	02/2005	Choi			
		6,920,634	07/2005	Tudor			

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS

EXAMINER INITIAL		DESCRIPTION (Including Author, Title, Date, Pertinent Pages, Etc.)
	*	I2O Special Interest Group "Intelligent I/O (I2O) Architecture Specification Version 2.0", Online!, March 1999, pp. 1-98, http://embedded.ifmo.ru/lib/DOC/RTOS/I2O/V2.0_spec.pdf
	*	Ryan, "Synchronization in Portable Device Drivers", Operating Systems Review (SIGOPS), ACM Headquarters, Vol. 32, No. 4, October 1998, pp. 62-69.
		LeCarme et al. "Software Portability." McGraw-Hill Company, New York, 1986, pp. 1-219.
		DuBois, Paul. "Software Portability with Imake," Second Edition, O'Reilly & Assoc., 1996, Whole Manual.
		Nu-Mega Technologies, Inc. (Company Literature), "Bounds-Checker User's Guide," Chapter 6, "Customizing Data Checking with Routine-Dat," July 21, 1998, pp. 75-83.
		Nilsson, Hans-Peter. "Porting the GNU C Compiler for Dummies," December 1, 1998, 96 pages.
		Wolf et al. "Portable Compiler Eases Problems of Software Migration." System Design/Software, 1984, pp. 147-153.
		Steffen, Joseph L. "Adding Run-Time Checking to the Portable C Compiler." John Wiley & Sons, October 21, 1991, pp. 305-316.
		Roberts et al. "A Portable Assembler Reverse Engineering Environment (PARE)," IEEE WCRE'96, pp. 76-85.

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EXAMINER INITIAL		DESCRIPTION (Including Author, Title, Date, Pertinent Pages, Etc.)
		Maguire, Steve. "Writing Solid Code." Microsoft Press, 1993, pp. 111-117.
		Chung-Hwg Herman Rao, et al, "iMobile: A Proxy-Based Platform for Mobile Services", ACM, 2001, pages 3-10.
		Ravi A. Dirckze et al., "A Pre-Serialization Transaction Management Technique For Mobile Multidatabases", 2000, Mobile Networks and Applications, pages 311-321.
		Nikos A Nikolaou et al., "Wireless Convergence Architecture: A Case Study Using GSM and Wireless LAN", 2002, pages 259-267.
	*	National Semiconductor, Santa Clara, California, "DS90CR211/DS90CR212 21-Bit Channel Link", USA, XP002306540; URL: http://www.national.com/ds/DS/DS90CR211.pdf , pages 1-2; figure 13.
	*	Novak T et al., "Channel Link-Moving and Shaping Information In Point-to-Point Applications", May 1996, National Semiconductor, Santa Clara, California, USA, XP002306541, URL: http://www.natiional.com/an/AN/AN-1041.pdf , pages 1-2, figures 1-5.
	*	Von Herzen B et al., "Multi-Channel 622 Mb/s LVDS Data Transfer for Virtex-E Devices", January 6, 2001, Xilinx Inc., San Jose, California, USA XP002306542, URL: http://direct.xilinx.com/bvdocs/appnotes/xapp233.pdf , pages 1-5, figures 1-3, and 6.
	*	Kitanowska S et al, "Bus LVDS with Virtex-E Devices: July 26, 2000, Xilinx Inc., San Jose, California, USA, XP002307032, URL: http://direct.xilinx.com/bvdocs/appnotes/xapp243.pdf .

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